



PATENT
SZS&Z Ref. No.: IO030911PUS
Attorney Docket No.: INFN/SZ0021

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Jong-Hoon Oh

Serial No.: 10/675,549

Confirmation No.: 6527

Filed: 9/30/2003

For: ECHO CLOCK ON MEMORY
SYSTEM HAVING WAIT
INFORMATION

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Group Art Unit: 2824

Examiner: Van Thu Nguyen

Customer No.: 046798

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**P.O. Box 1450**  
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July 27, 2005  
Date

Randol W. Read

Dear Sir:

## INFORMATION DISCLOSURE STATEMENT

The Applicant(s) and the Attorney who signs below on the basis of the information supplied by the inventor(s) and the information in his file submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

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In accordance with 37 CFR § 1.97, this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possibly material information as defined under 37 CFR § 1.56(a) exists.

The patents and/or publications submitted herewith are set forth on the attached Form PTO-1449.

Each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. A copy of the International Search Report and Written Opinion dated June 8, 2005, is enclosed.

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Respectfully submitted,



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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

|                        |                |
|------------------------|----------------|
| Application Number     | 10/675,549     |
| Filing Date            | 9/30/2003      |
| First Named Inventor   | Jong-Hoon Oh   |
| Group Art Unit         | 2824           |
| Examiner Name          | Van Thu Nguyen |
| Attorney Docket Number | INFN/SZ0021    |
| Submission Date        | July 27, 2005  |

Sheet 1

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## **U.S. PATENT DOCUMENTS**

| Examiner<br>Initials* | Cite<br>No. <sup>1</sup> | Document Number                          | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines, Where<br>Relevant Passages or Relevant<br>Figures Appear |
|-----------------------|--------------------------|------------------------------------------|--------------------------------|----------------------------------------------------|---------------------------------------------------------------------------------|
|                       |                          | Number-Kind Code <sup>2</sup> (if known) |                                |                                                    |                                                                                 |
|                       | A1                       | US-                                      |                                |                                                    |                                                                                 |
|                       | A2                       | US-                                      |                                |                                                    |                                                                                 |
|                       | A3                       | US-                                      |                                |                                                    |                                                                                 |
|                       | A4                       | US-                                      |                                |                                                    |                                                                                 |
|                       | A5                       | US-                                      |                                |                                                    |                                                                                 |
|                       | A6                       | US-                                      |                                |                                                    |                                                                                 |
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|                       | A12                      | US-                                      |                                |                                                    |                                                                                 |

## **FOREIGN PATENT DOCUMENTS**

| Examiner<br>Initials* | Cite<br>No. <sup>1</sup> | Foreign Patent Document                  | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines,<br>Where Relevant Passages or<br>Relevant Figures Appear | T <sup>3</sup> |
|-----------------------|--------------------------|------------------------------------------|--------------------------------|----------------------------------------------------|---------------------------------------------------------------------------------|----------------|
|                       |                          | Number-Kind Code <sup>2</sup> (if known) |                                |                                                    |                                                                                 |                |
|                       | B1                       | EP 0 344 886 A1                          | 12-06-1989                     | International<br>Computers Limited                 |                                                                                 |                |
|                       | B2                       |                                          |                                |                                                    |                                                                                 |                |
|                       | B3                       |                                          |                                |                                                    |                                                                                 |                |
|                       | B4                       |                                          |                                |                                                    |                                                                                 |                |
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Examiner

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| Attorney Docket Number | INFN/SZ0021    |
| Submission Date        | July 27, 2005  |

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of 2

## **NON PATENT LITERATURE DOCUMENTS**

| Examiner Initials* | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | T <sup>2</sup> |
|--------------------|-----------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
|                    | C1                    | PCT International Search Report and Written Opinion dated June 8, 2005.                                                                                                                                                                                        |                |
|                    | C2                    | <i>General DDR SDRAM Functionality</i> , Micron Technical Note TN 46-05, Micron Technology, Inc., July 2001, pp. 1-11.                                                                                                                                         |                |
|                    | C3                    | <i>KDM432D2131 – 64Mbit DDR SGRAM, 512K x 32Bit x 4 Banks, Double Data Rate Synchronous Graphic RAM with Bi-directional Data Strobe</i> , Samsung Electronics Target Specification, Revision 0.3, May 1999, pp. 1-49.                                          |                |
|                    | C4                    | C. H. KIM et al., <i>A 64-Mbit, 640-MByte/s Bidirectional Data Strobed, Double-Data-Rate SDRAM with a 40-mW DLL for a 256-Mbyte Memory System</i> , IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, pp. 1703-1709, Nov. 1998.                           |                |
|                    | C5                    |                                                                                                                                                                                                                                                                |                |
|                    | C6                    |                                                                                                                                                                                                                                                                |                |
|                    | C7                    |                                                                                                                                                                                                                                                                |                |
|                    | C8                    |                                                                                                                                                                                                                                                                |                |
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